L	Hits	S arch T xt	DB	Tim stamp
lumber	100	("5247455"	USPAT;	2002/09/04
		"5321885"	US-P PUB;	13:59
		"5628852"	EP ; JP ;	_
		"5686702"	DERWENT;	
		"4587727"	IBM_TDB	
		"4763407"	_	
		"5268813"		
,		"5534666"		
		"5590461"		
		"4314115"		
		"4536051"	i	
,		"4801876"		
·		"5512712"		
		"5982468"		
		"6085018"		
		"5262954"		
		"6021025"		
		"4510446"		
		"4549206"		
		"4782283"		
		"5325582"		
		"5558522"		
		"5621913"		
		"5784484"		
		"5909452"		
		"6147870"		
		"6185273"		
		"5008619"		
,		"5821614"		
		"4627005"		
		"5530622"		
		"5718038"		
	ŗ,	"5743990"		
		"6144557"		
		"4285123"		
		"4404059"		
		"4628598"		
		"4759809"		
		"4790895"		
		"4806957"		
		"4863550"		
		"5206463"		
		"5671531"		
		"5685071"		
		"5710693"		
,		"5788855"		
		"5834705"		
		"5940964"		
		"6053395"		
		"6061260").pn.	1	

2	137246	"print d ircuit b ard"	USPAT;	2002/09/04
			EP ; JP ;	14:47
			DERWENT;	
			US CR	
3	605	"print d cir uit b ard" and sch duling	USPAT;	2002/09/04
			EP ; JP ;	14:47
			DERWENT;	
			USOCR	
1	179	("printed circuit board" and scheduling) and	USPAT;	2002/09/04
		layout	EPO; JPO;	14:47
			DERWENT;	
			USOCR	
5	10	((("printed circuit board" and scheduling)	USPAT;	2002/09/04
		and layout) and assembly) and CAD	EPO; JPO;	14:48
			DERWENT;	
			USOCR	
5	99	(("printed circuit board" and scheduling) and	USPAT;	2002/09/04
		layout) and assembly	EPO; JPO;	14:59
			DERWENT;	
			USOCR	
7	0	("printed circuit board" and scheduling) and	USPAT;	2002/09/04
		"board arrangment"	EPO; JPO;	15:00
			DERWENT;	
			USOCR	

8	0	(("5247455"	USPAT;	2002/09/04
•		"5321885"	EP ; JP ;	15:00
		"5628852"	DERWENT;	10100
		"5686702 "	US CR	-8-
		"4587727"	33 31	
		"4763407"		
		"5268813 "		
		"5534666"		
		"5590461"		
		"4314115"	`*·	
		"4536051"		
		"4801876"		
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ļ.		"5262954"		
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		"4782283"		
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		"5558522"		
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		"5743990"		
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		"4628598"		
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		"4863550"		
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		"5671531"		
		"5685071"		
		"5710693"	1	
		"5788855"		
		"5834705"		
1		"5940964"		
		"6053395"		
		"6061260").pn.		
1) and "b ard arrangm nt"		

9	351	("print d circuit b ard" and sch duling) and	USPAT;	2002/09/04
		arrang m nt	EP ; JP ;	15:00
			DERWENT;	
			US CR	
0	0	(("print d ircuit b ard" and scheduling) and	USPAT;	2002/09/04
		arrang m nt) and (pcb near arrang m nt)	EP ; JPO;	15:01
•			DERWENT;	ļ
			USOCR	
11	6	(("printed circuit board" and scheduling) and	USPAT;	2002/09/04
		arrangement) and (board near arrangement)	EPO; JPO;	15:02
			DERWENT;	
			USOCR	
12	31395	700/\$.ccls.	USPAT;	2002/09/04
			EPO; JPO;	15:03
			DERWENT;	
			USOCR	:
13	48	("printed circuit board" and scheduling) and	USPAT;	2002/09/04
		700/\$.ccis.	EPO; JPO;	15:12
			DERWENT;	
			USOCR	
14	222473	29/\$.ccis.	USPAT;	2002/09/04
			EPO; JPO;	15:13
			DERWENT;	
à			USOCR	

15	17	29/\$.ccls. and (("5247455"	USPAT;	2002/09/04
		"5321885"	EPO; JPO;	15:15
		"5628852"	DERWENT;	
		"5686702"	US CR	
		"4587727"		
		"4763407"		
		"5268813"		
		"5534666"		
		"5590461"		
		"4314115"		
		"4536051"		
		"4801876"		
		"5512712"		
		"5982468"		
		"6085018"		
,		"5262954"		
		"6021025"		
		"4510446"		
		"4549206"		
		"4782283"		
		"5325582"		
		"5558522"		
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		"5718038"		
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		"5743990"		
		"6144557"		
		"4285123"		
		"4404059"		1
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		"4759809"		
		"4790895"		
		"4806957"	4	
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		"5206463"		
		"5671531"		
		"5685071"		
		"5710693"		
		"5788855"		
		"5834705"		
		"5940964"		
		"6053395"		
		"6061260").pn.		
)		

40		"C rh r odi Come n4"	HCDAT-	2002/00/04
16	0	"G rb r adj Garm nt"	USPAT; EPO; JPO;	2002/09/04 15:15
			DERWENT;	13.13
			USOCR	
17	541	rb r adj Garm nt	USPAT;	2002/09/04
18	341	ib rady Saini iit	EPO; JPO;	15:15
			DERWENT;	13.13
			USOCR	
19	0	((Gerber adj Garment) and waste) and	USPAT;	2002/09/04
		"waste minimization"	EPO; JPO;	15:16
			DERWENT;	10110
			USOCR	
18	55	(Gerber adj Garment) and waste	USPAT;	2002/09/04
		(Construction) and traces	EPO; JPO;	15:21
			DERWENT;	
			USOCR	
20	185	cutting near "circuit boards"	USPAT;	2002/09/04
			EPO; JPO;	15:22
			DERWENT;	
			USOCR	
21	8	(cutting near "circuit boards") and waste	USPAT;	2002/09/04
			EPO; JPO;	15:26
			DERWENT;	
•			USOCR	
22	0	(cutting near "circuit boards") and	USPAT;	2002/09/04
		scheduling	EPO; JPO;	15:26
			DERWENT;	
			USOCR	
23	0	(cutting near "circuit boards") and schedul	USPAT;	2002/09/04
			EPO; JPO;	15:26
			DERWENT;	
			USOCR	
24	34	(cutting near "circuit boards") and	USPAT;	2002/09/04
		production	EPO; JPO;	15:30
		•	DERWENT; USOCR	
25	027	BCB moor manufacturing	USOCK USPAT;	2002/09/04
25	837	PCB near manufacturing	EPO; JPO;	15:30
			DERWENT;	13.30
			USOCR	
26	390	(PCB near manufacturing) and (printed near	USPAT;	2002/09/04
20	350	circuit)	EPO; JPO;	15:31
			DERWENT;	
			USOCR	
27	70	((PCB near manufacturing) and (printed near	USPAT;	2002/09/04
		circuit)) and cut	EPO; JPO;	15:37
			DERWENT;	
			USOCR	
28	204	"printed wiring board ass mbli s"	USPAT;	2002/09/04
		-	EP ; JP ;	15:39
			DERWENT;	
			USOCR	

29	63	"print d wiring b ard ass mbli s" and	USPAT;	2002/09/04
		manufacturing	EP ; JPO;	15:39
			DERWENT;	
			US CR	
30	4	("printed wiring b ard assembli s" and	USPAT;	2002/09/04
		manufacturing) and "manufacturing system"	EP ; JPO;	15:43
			DERWENT;	
			USOCR	
31	10	"5321885"	USPAT;	2002/09/04
			EPO; JPO;	15:44
			DERWENT;	
			USOCR	
32	14	5516028.URPN.	USPAT	2002/09/04
				15:51
33	23	("3421211" "3803556" "4256296"	USPAT	2002/09/04
'		"4504919" "4564102" "4604704"		15:58
ļ		"4694570" "4706187" "4719694"		
		"4731567" "4759488" "4821197"		
		"4850104" "4980970" "5067648"		
		"5086557" "5170554" "5268849"		
		"5321885" "5335601" "5355579"		
		"5536128" "5680696").PN.		